

Application No. 09/591,044  
Filed: June 9, 2000  
Group Art Unit: 2181

### In the Claims

Please rewrite claims 1 and 6 to read as follows:

1. (Amended) A system for transferring data between a plurality of devices, comprising:

a bus including at least one data line for transmitting the data and at least one clock line;

a first device communicably coupled to the bus; and

A a second device communicably coupled to the bus,

wherein the system is operative at a first clock rate and at a second clock rate less than the first clock rate,

wherein the first device is operative at least at the second clock rate to receive at least a portion of the data transmitted over the data line, and to store the at least a portion of the data in a register, and

wherein the second device is operative at least at the second clock rate to drive the clock line to a low logic level while the data is stored in the register of the first device.

A<sup>2</sup> 6. (Amended) A method of transferring data between a plurality of devices, comprising the steps of: